



FIG. 1

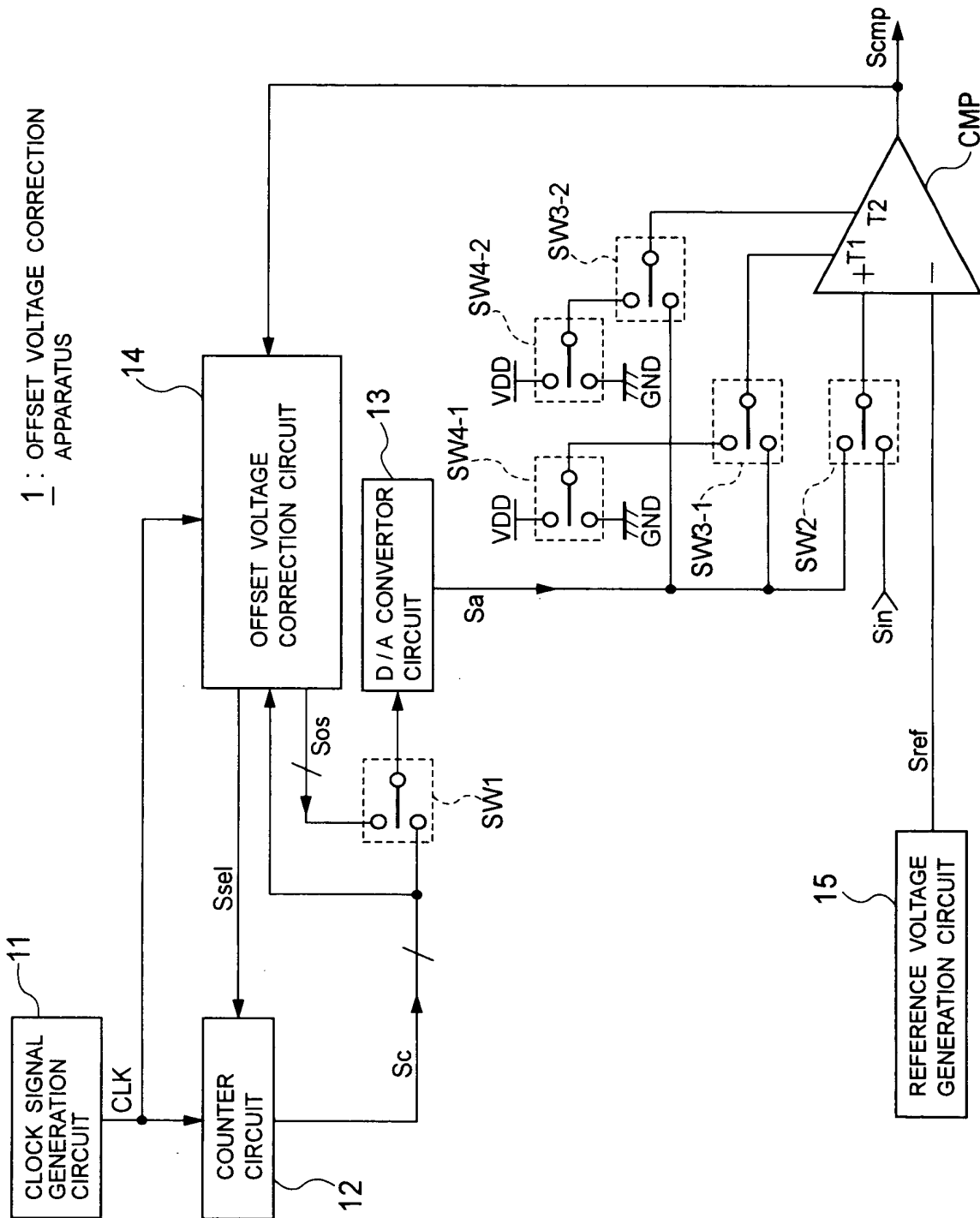


FIG. 2

12 : COUNTER CIRCUIT

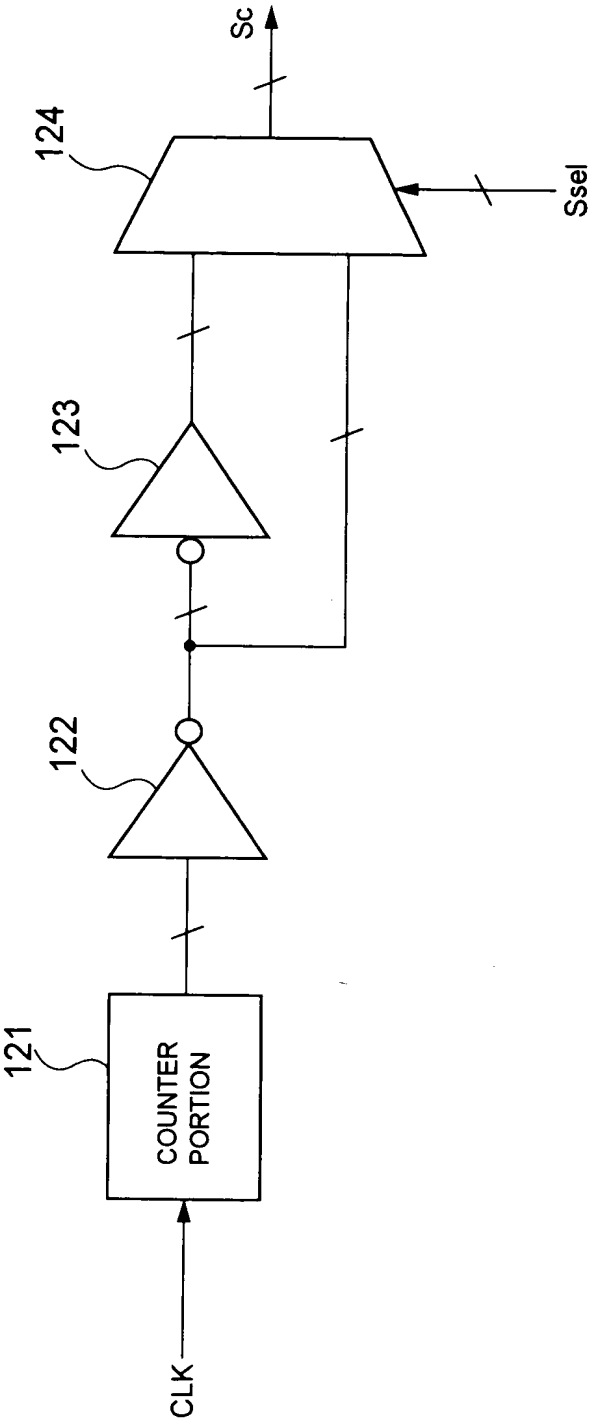


FIG. 3

14 : OFFSET VOLTAGE CORRECTION CIRCUIT

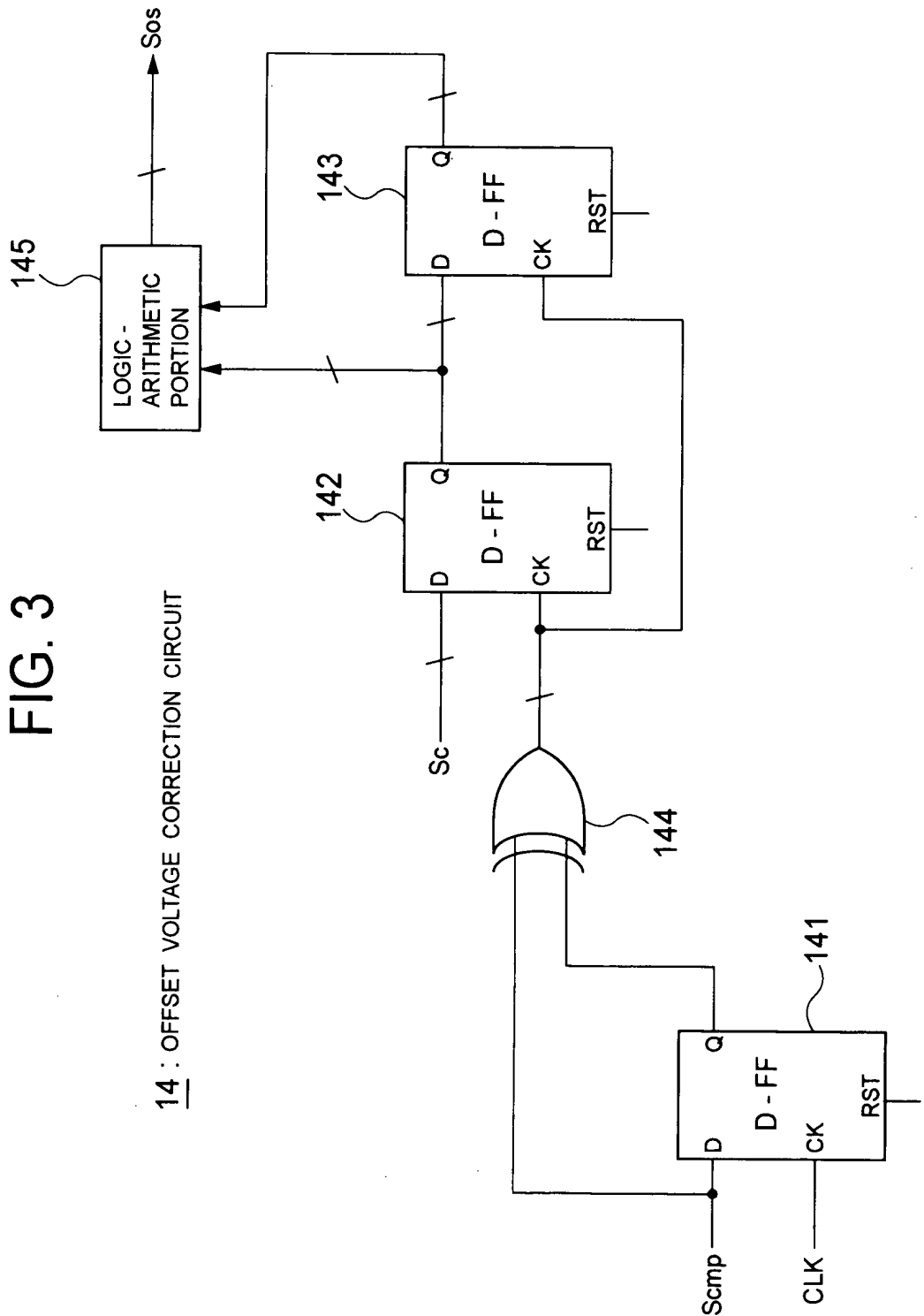


FIG. 4

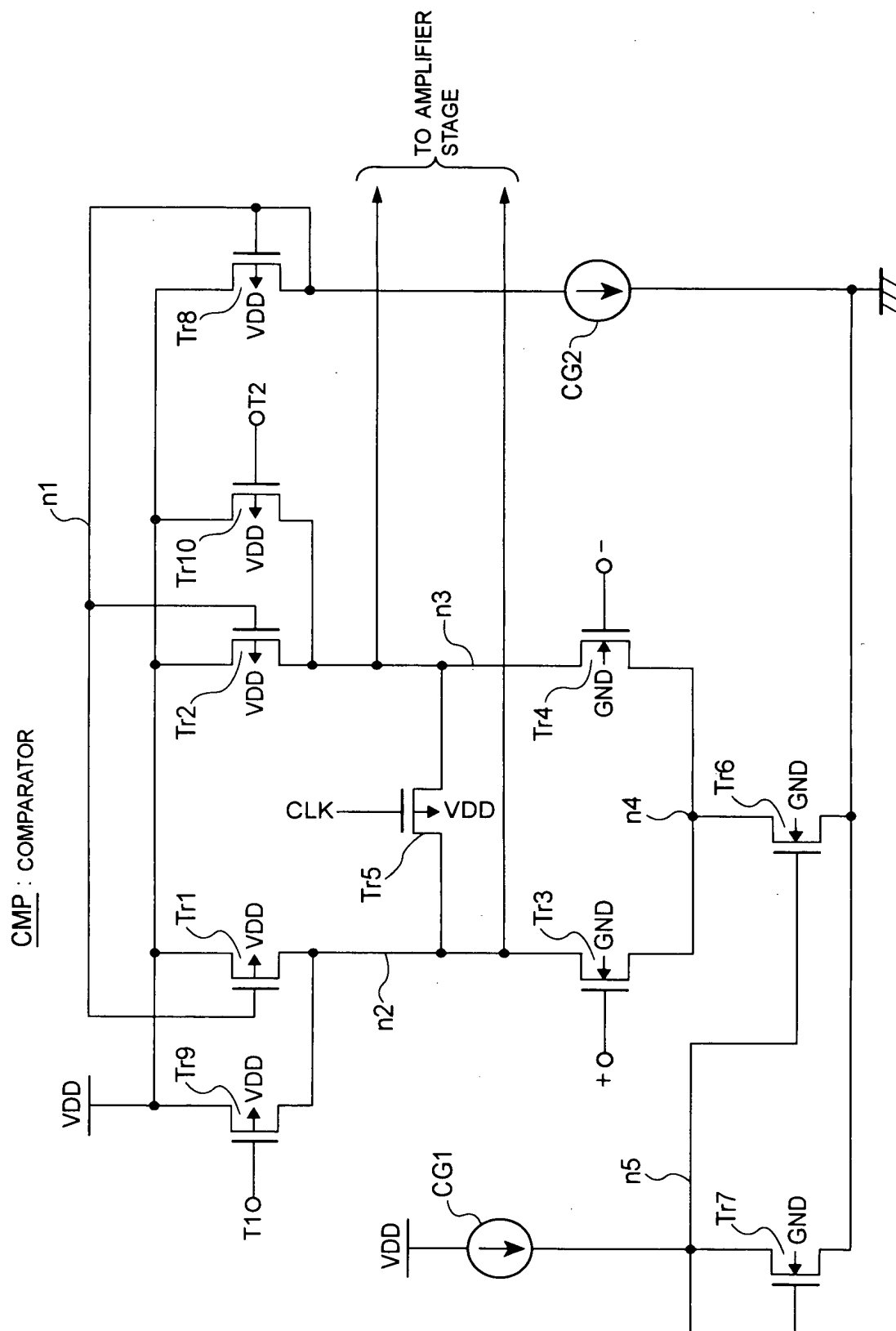


FIG. 5

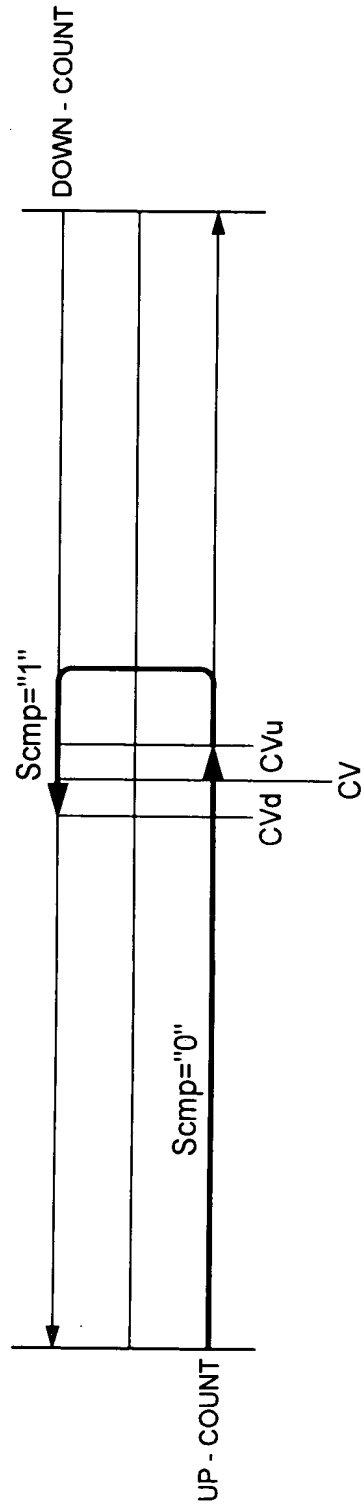


FIG. 6

145 : LOGIC - ARITHMETIC PORTION

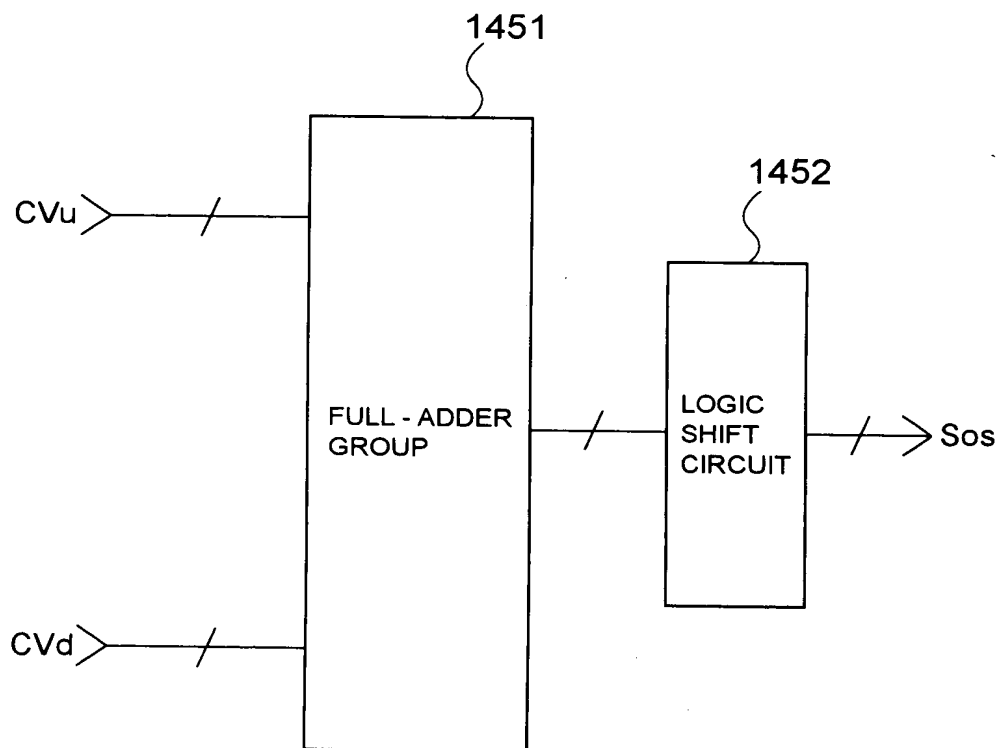


FIG. 7

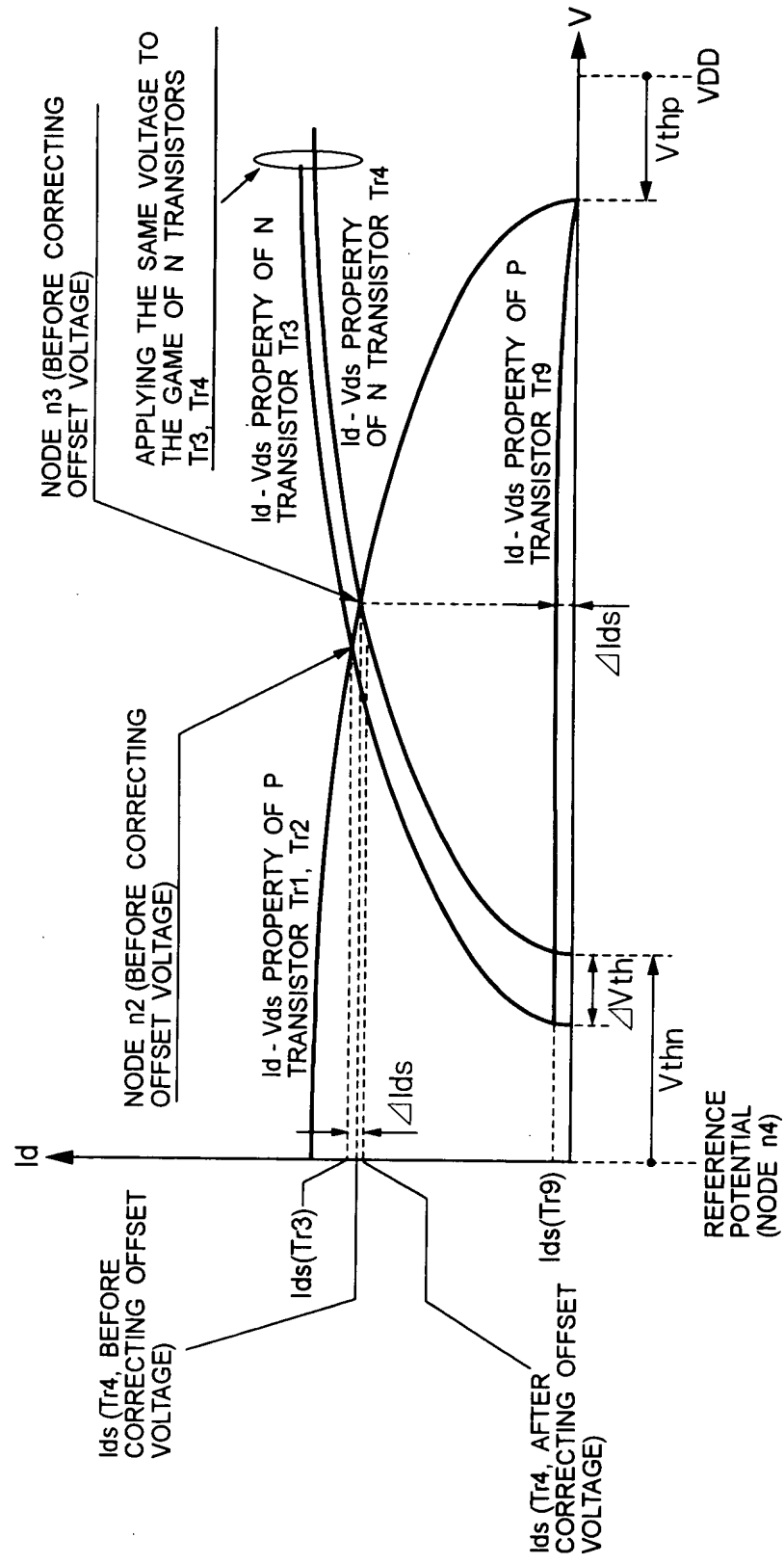


FIG. 8

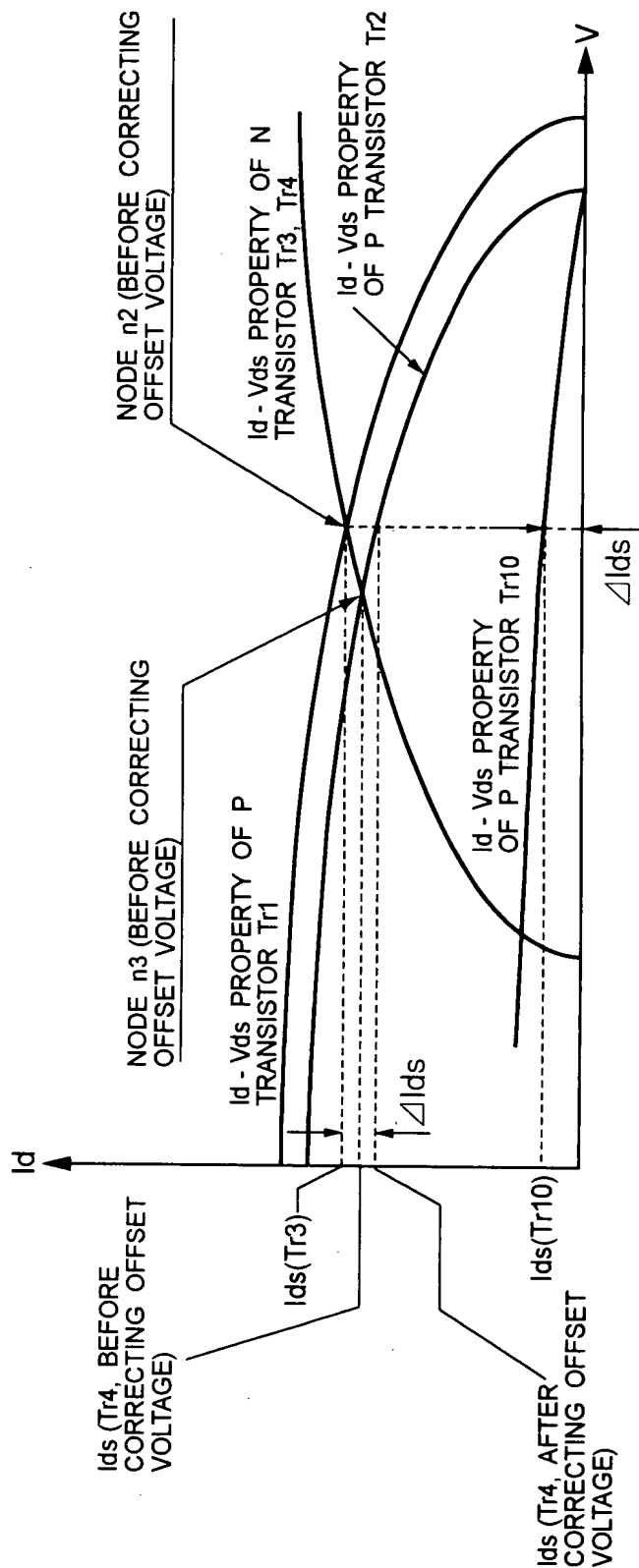






FIG. 10

